

[SWAN]

DVT Schematic & Layout Changing Request

Organization
R&D Lab. 2

Issued Date
2020.10.22

Version
1.0

Revision History

Version	Date	Description	Owner
1.0	2020/10/22	1 st Draft version	Lonnie Kim

1. Review Result

2. Action Items

SoC Board



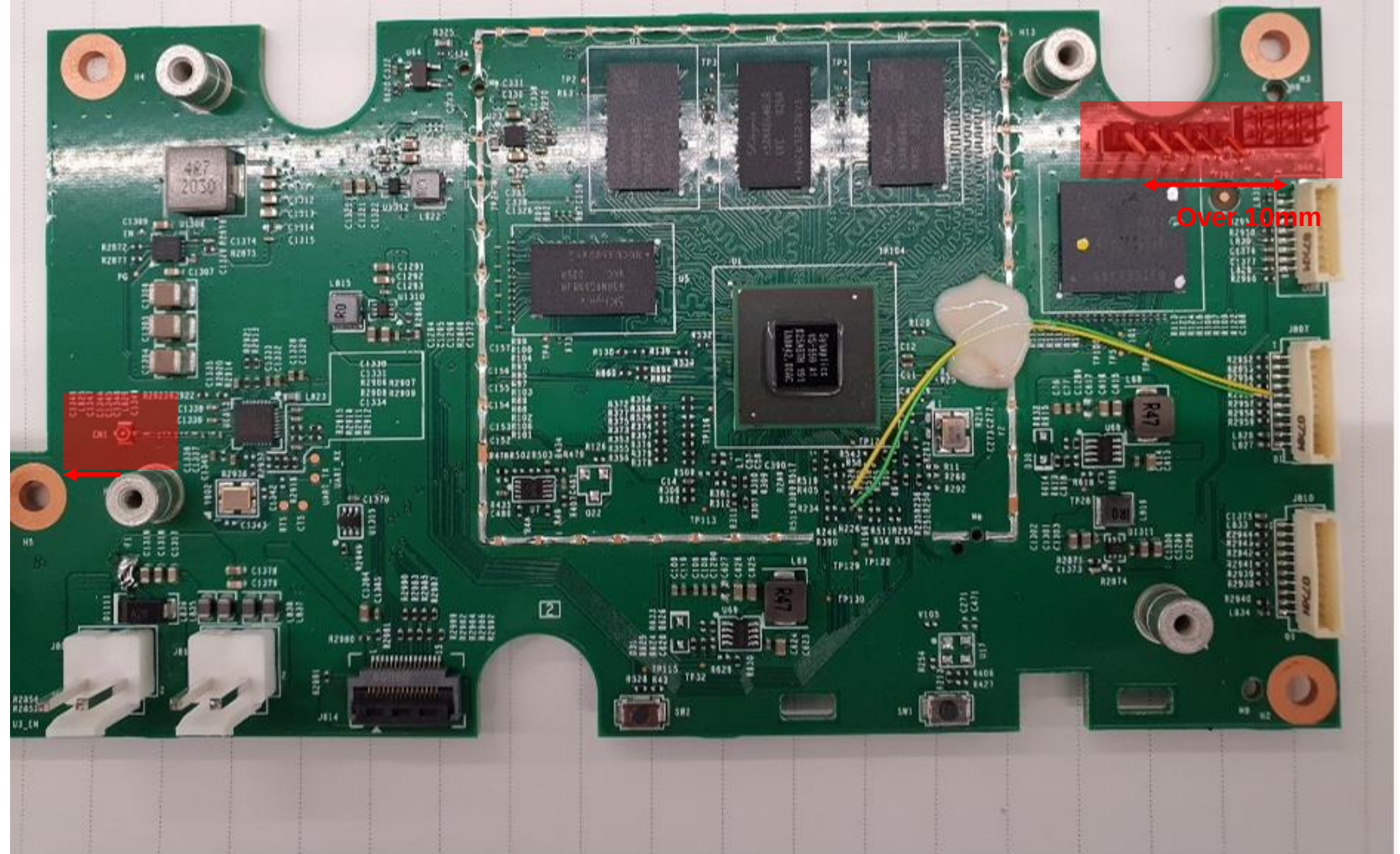
Schematic Request

- **Add HW revision resistors(2 GPIO and pull-up/down resistors)**
- **Add circuit(components) for reduce DDR power noise(Synaptics recommend)**
- **Modify HDMI 4K 60frame**
- **Modify BT interface and add USB HUB**
- **Modify EVT rework points**
- **Add FET at 24V power line for reduce standby power consumption**



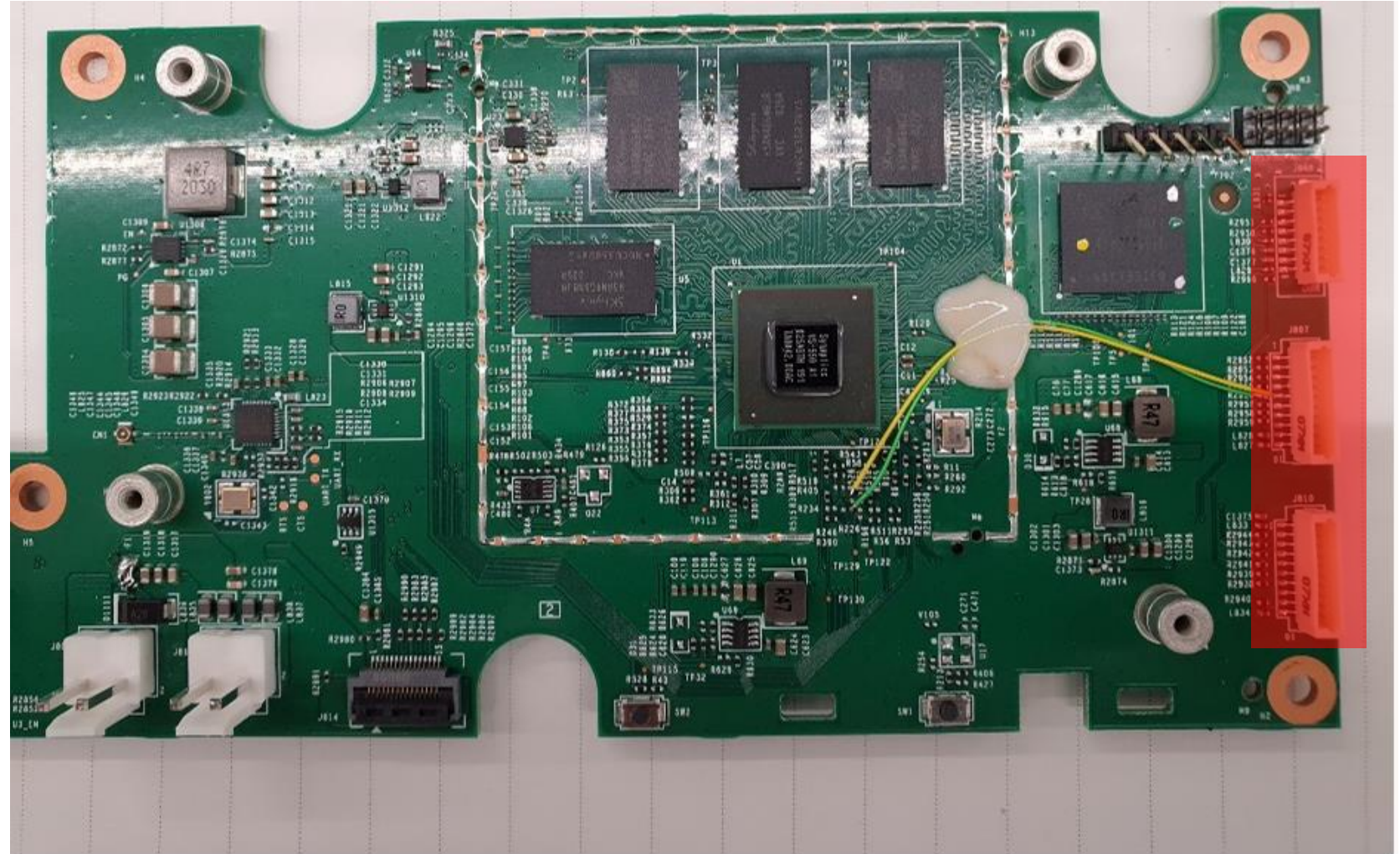
Layout Request

- **J4 and J6 distance : over 10mm**
 - Connector distance should be over 10mm.
- **CN1(BT connector)**
 - CN1 should be move left



Other Request

- J807, J808, J810 : Check possibility of change to FFC connectors?



Amp Board





Layout Request

- **Connectors for Speaker**

- J802, J803, J804: Add to Silk Speaker name(Woofer/Left/Right)

- **Connectors moving from Top to Bottom layer**

- J802, J803, J804, J813: Location change Top to Bottom side to easy assembly

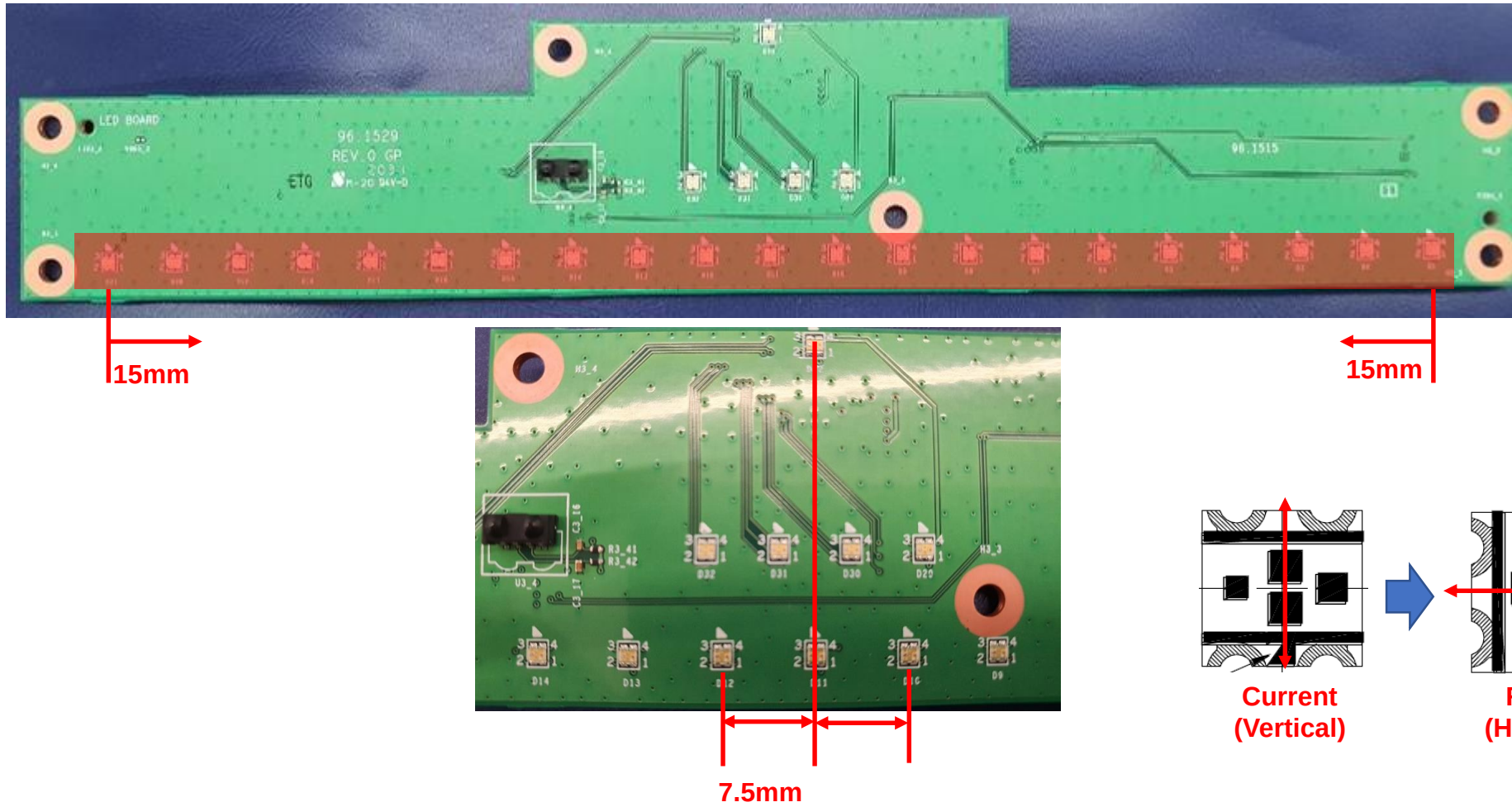
LED Board



Layout Request

- LED distance from 9mm to 7.5mm(D1~D21) and Horizontal direction

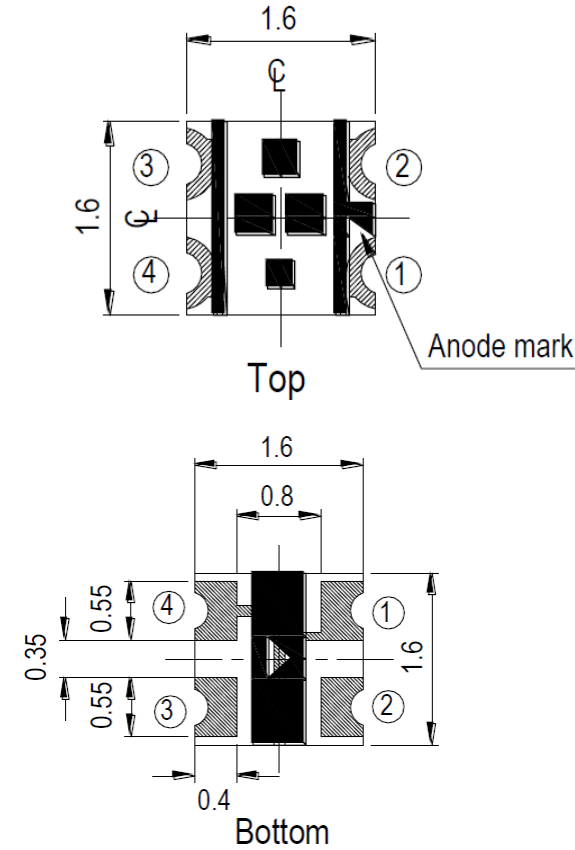
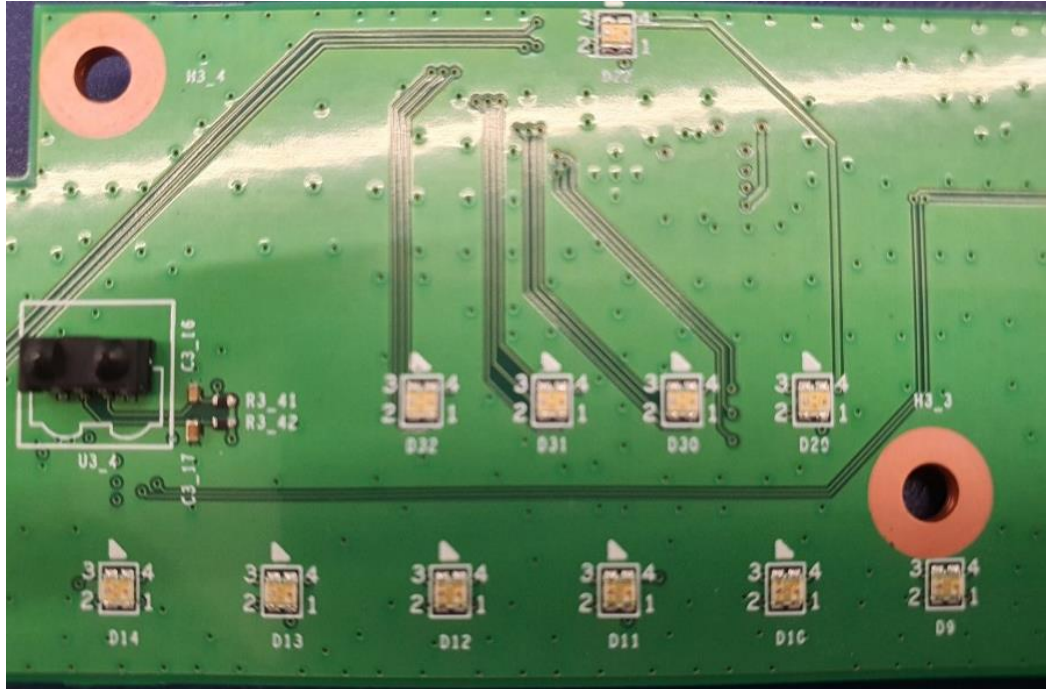
- D1~D21: Distance(9mm → 7.5mm)
- D1~D21: Direction(Vertical → Horizontal)





Layout Request

- LED Pin silk need to be changed
 - Current silk referred from bottom view(change to top view)



Thank you.